

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mikolajick, *et al.* Docket No.: 2003 P 54276 US
Filed: November 17, 2003 Examiner: TBD
Serial No.: 10/715,142 Art Unit: 2818
For: Charge-Trapping Memory Device and Methods for Operating and Manufacturing the Cell

Mail Stop: Missing Parts
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A & 08B that may be considered material to the examination of the above-identified application.

Pursuant to 37 C.F.R. 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of the foreign patents and non-patent literature.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

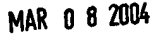
Respectfully submitted,

Ira S. Matsil
Attorney for Applicant
Reg. No. 35,272

March 3, 2004

Date

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
(972) 732-1001 (phone)
(972) 732-9218 (fax)



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Attorney Docket Number	2003 P 54276 US
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Sheet	1	of	2
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				<i>Application Number</i>	10/715,142
				<i>Filing Date</i>	November 17, 2003
				<i>First Named Inventor</i>	Mikolajick, et al.
				<i>Art Unit</i>	2818
				<i>Examiner Name</i>	TBD
				<i>Attorney Docket Number</i>	2003 P 54276 US
Sheet	2	of	2		

[illegible]

Examiner Signature		Date Considered	
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